

Docket No. 215544US2

IN RE APPLICATION OF: Takuji MATSUMOTO, et al.

SERIAL NO: 09/986,004

FILED: November 7, 2001

FOR: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME



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ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

- ☐ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☒ Additional documents filed herewith: Marked-Up Copy; Request for Extension of Time (1 month); English Translation of Priority Document No. JP2001-035180, Filed on February 13, 2001; Information Disclosure Statement; Form PTO 1449; Reference Cited (1)

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	19	MINUS	20	0	x \$18 =	\$0.00
INDEPENDENT	6	MINUS	6	0	x \$84 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$280 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
		TOTAL				\$0.00

- ☒ A check in the amount of **\$290.00** is attached.
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,
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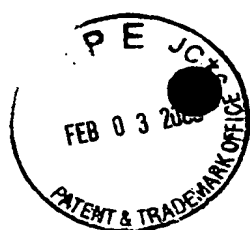


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Takuji MATSUMOTO, et al.

SERIAL NO: 09/986,004

FILED: NOVEMBER 7, 2001

FOR: SEMICONDUCTOR DEVICE AND
METHOD OF MANUFACTURING
THE SAME

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: GROUP: 2826

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: EXAMINER: SEFER, A.

:

#15/02
2/12/03
Schmidt
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AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Office Action dated October 3, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATION

Please amend the specification at page 1, line 19 through page 2, line 7 as shown below:¹

a' The complete isolation technique is characterized by being latch up free (latch - up is not caused), and resistant to noise and the like because the element is electrically isolated completely from other elements. However, since a transistor is operated in an electrical floating state, there is a problem in that a frequency dependency is caused on a delay time and a floating-body effect, for example, a kink effect in which a hump is generated on a drain

¹ A marked-up copy of the amendments is attached hereto.